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1 [Reconciling responsiveness with performance in pure object-oriented languages](#)

 Urs Hözle, David Ungar

July 1996 **Transactions on Programming Languages and Systems (TOPLAS)**, 19(3)

Publisher: ACM 

Full text available:  Pdf (537.19 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#)

Bibliometrics: Downloads (6 Weeks): 5, Downloads (12 Months): 50, Downloads (Overall): 42

Dynamically dispatched calls often limit the performance of object-oriented programs. This programming encourages factoring code into small, reusable units, thereby increasing expensive operations. Frequent calls ...

Keywords: adaptive optimization, pause clustering, profile-based optimization, run-

2 [A retargetable, ultra-fast instruction set simulator](#)

 Jianwen Zhu, Daniel D. Gajski

January 1999 **DATE '99: Proceedings of the conference on Design, automation and test**

Publisher: ACM

Full text available:  Pdf (57.35 KB)

Additional Information: [full citation](#), [references](#), [cited by](#)

Bibliometrics: Downloads (6 Weeks): 6, Downloads (12 Months): 40, Downloads (Overall): 15

3 [The Jrpm system for dynamically parallelizing Java programs](#)

 Michael K. Chen, Kunle Olukotun

June 2003 **ISCA '03: Proceedings of the 30th annual international symposium on Computer architecture**

Publisher: ACM

Full text available:  Pdf (320.42 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#)

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 67, Downloads (Overall): 58

We describe the Java runtime parallelizing machine (Jrpm), a complete system for parallelizing Java programs automatically. Jrpm is based on a chip multiprocessor (CMP) with thread-level speculation, low sharing and communication ...

Also published in:

May 2003 **SIGARCH Computer Architecture News** Volume 31 Issue 2

4 [Fast detection of communication patterns in distributed executions](#)

Thomas Kunz, Michiel F. H. Seuren

November 1997 **CASCON '97: Proceedings of the 1997 conference of the Centre for Advanced Computing and Communications**

Publisher: IBM Press

Full text available:  Pdf (4.21 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#)

Bibliometrics: Downloads (6 Weeks): 25, Downloads (12 Months): 309, Downloads (Overall): 100

Understanding distributed applications is a tedious and difficult task. Visualizations are often used to obtain a better understanding of the execution of the application. T Poet, an event ...

5 Design flow for HW / SW acceleration transparency in the thumbpod secure emb

◆ David Hwang, Bo-Cheng Lai, Patrick Schaumont, Kazuo Sakiyama, Yi Fan, Shenglin Yan, Verbauwhede

June 2003 **DAC '03: Proceedings of the 40th annual Design Automation Conference**

Publisher: ACM 

Full text available:  Pdf (250.69 KB)

Additional Information: [full citation](#), [abstract](#), [references](#),

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 42, Downloads (Overall): 55

This paper describes a case study and design flow of a secure embedded system called cryptographic and biometric signal processing acceleration. It presents the concept of transparency, a systematic method to accelerate ...

6 TOSSIM: accurate and scalable simulation of entire TinyOS applications

◆ Philip Levis, Nelson Lee, Matt Welsh, David Culter

◆ November 2003 **SenSys '03: Proceedings of the 1st international conference on Embedd**

Publisher: ACM 

Full text available:  Pdf (429.79 KB)

Additional Information: [full citation](#), [abstract](#), [references](#),

Bibliometrics: Downloads (6 Weeks): 64, Downloads (12 Months): 350, Downloads (Overall):

Accurate and scalable simulation has historically been a key enabling factor for system design, TOSSIM, a simulator for TinyOS wireless sensor networks. By exploiting the sensor network design, TOSSIM can capture network ...

Keywords: TOSSIM, sensor networks, tinyOS

7 Continuous program optimization: A case study

◆ Thomas Kistler, Michael Franz

◆ July 2003 **Transactions on Programming Languages and Systems (TOPLAS)**, Vol.

Publisher: ACM 

Full text available:  Pdf (877.67 KB)

Additional Information: [full citation](#), [abstract](#), [references](#),

Bibliometrics: Downloads (6 Weeks): 16, Downloads (12 Months): 155, Downloads (Overall):

Much of the software in everyday operation is not making optimal use of the hardware. Among the reasons for this discrepancy are hardware/software mismatches, modular by software engineering considerations, ...

Keywords: Dynamic code generation, continuous program optimization, dynamic re

8 The Purdue University network-computing hubs: running unmodified simulation t

◆ Nirav H. Kapadia, José A. B. Fortes, Mark S. Lundstrom

◆ January 2000 **Transactions on Modeling and Computer Simulation (TOMACS)**, Vol.

Publisher: ACM 

Full text available:  Pdf (110.49 KB)

Additional Information: [full citation](#), [abstract](#), [references](#),

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 32, Downloads (Overall): 36

This paper describes the Web interface management infrastructure of a functioning (PUNCH) that allows users to run unmodified simulation packages at geographically distributed sites. It currently contains more than fifty ...

Keywords: Internet computing, network-computing, web-based simulation

9 Process migration

Dejan S. Milošić, Fred Dougis, Yves Paindaveine, Richard Wheeler, Songnian Zhou
September 2000 **Computing Surveys (CSUR)**, Volume 32 Issue 3

Publisher: ACM  [Request Permissions](#)

Full text available:  [Pdf](#) (1.24 MB)

Additional Information: [full citation](#), [abstract](#), [references](#),

Bibliometrics: Downloads (6 Weeks): 95, Downloads (12 Months): 650, Downloads (Overall):

Process migration is the act of transferring a process between two machines. It enables fault resilience, eases system administration, and data access locality. Despite these efforts, migration has ...

Keywords: distributed operating systems, distributed systems, load distribution, prc

10 Effects of clock resolution on the scheduling of interactive and soft real-time processes

Yoav Etsion, Dan Tsafrir, Dror G. Feitelson

June 2003 **SIGMETRICS '03: Proceedings of the 2003 ACM SIGMETRICS international conference on modeling and modeling of computer systems**

Publisher: ACM  [Request Permissions](#)

Full text available:  [Pdf](#) (512.91 KB)

Additional Information: [full citation](#), [abstract](#), [references](#),

Bibliometrics: Downloads (6 Weeks): 9, Downloads (12 Months): 63, Downloads (Overall): 90

It is commonly agreed that scheduling mechanisms in general purpose operating systems support for modern interactive applications, notably multimedia applications. The core is to devise specialized scheduling ...

Keywords: Linux, clock interrupt rate, interactive process, overhead, scheduling, so

Also published in:

June 2003 **SIGMETRICS Performance Evaluation Review** Volume 31 Issue 1

11 Java driven codesign and prototyping of networked embedded systems

Josef Fleischmann, Klaus Buchenrieder, Rainer Kress

June 1999 **DAC '99: Proceedings of the 36th annual ACM/IEEE Design Automation Conference**

Publisher: ACM  [Request Permissions](#)

Full text available:  [Pdf](#) (69.24 KB)

Additional Information: [full citation](#), [references](#), [cited by](#), [index](#)

Bibliometrics: Downloads (6 Weeks): 12, Downloads (12 Months): 26, Downloads (Overall): 2

12 Optimizing indirect branch prediction accuracy in virtual machine interpreters

M. Anton Ertl, David Gregg

June 2003 **PLDI '03: Proceedings of the ACM SIGPLAN 2003 conference on Programming language design and implementation**

Publisher: ACM  [Request Permissions](#)

Full text available:  [Pdf](#) (190.05 KB)

Additional Information: [full citation](#), [abstract](#), [references](#),

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 72, Downloads (Overall): 64

Interpreters designed for efficiency execute a huge number of indirect branches and the execution time in indirect branch mispredictions. Branch target buffers are the best way to branch prediction; ...

Keywords: branch prediction, branch target buffer, code replication, interpreter, sup

Also published in:

May 2003 **SIGPLAN Notices** Volume 38 Issue 5

13 A hardware/software prototyping environment for dynamically reconfigurable emi
Josef Fleischmann, Klaus Buchenrieder, Rainer Kress

March 1998 **CODES/ CASHE '98: Proceedings of the 6th international workshop on Hard**
Publisher: IEEE Computer Society

Full text available:  Pdf (42.66 KB)

Additional Information: [full citation](#), [references](#), [cited by](#), [ind](#)

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 23, Downloads (Overall): 32

14 An integrated environment for testing mobile ad-hoc networks

 Yongguang Zhang, Wei Li

June 2002 **MobiHoc '02: Proceedings of the 3rd ACM international symposium on Mo**
computing

Publisher: ACM 

Full text available:  Pdf (315.26 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#)

Bibliometrics: Downloads (6 Weeks): 15, Downloads (12 Months): 111, Downloads (Overall): 111

Mobile Ad-Hoc Network (MANET) has become an increasingly active research area wi
routing, media access, and protocols, etc. However, much of the effort so far has bee
systems that have ever ...

Keywords: MANET, emulation, mobile ad hoc networks, multi-hop routing, packet fil

15 Virtual routers: a tool for networking research and education

 Florian Baumgartner, Torsten Braun, Eveline Kurt, Attila Weyland

July 2003 **SI GCOMM Computer Communication Review**, Volume 33 Issue 3

Publisher: ACM

Full text available:  Pdf (284.61 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#)

Bibliometrics: Downloads (6 Weeks): 9, Downloads (12 Months): 97, Downloads (Overall): 97

Virtual routers are software entities, i.e. user space processes, emulating IP routers o
computers. Virtual routers can be used for both networking research and education. I
virtual routers process packets ...

Keywords: distance learning, network emulation, networking, performance evaluati

16 DELI: a new run-time control point

Giuseppe Desoli, Nikolay Mateev, Evelyn Duesterwald, Paolo Faraboschi, Joseph A. Fisher
November 2002 **MICRO 35: Proceedings of the 35th annual ACM/IEEE international sym**

Publisher: IEEE Computer Society Press

Full text available:  Publisher Site,  Pdf (1.27 MB) Additional Information: [full citation](#), [abstract](#), [referenc](#)

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 29, Downloads (Overall): 31

The Dynamic Execution Layer Interface (DELI) offers the following unique capability:
over the execution of programs, by allowing its clients to observe and optionally man
--at run time---just ...

Useful downloads: [Adobe Acrobat](#) [QuickTime](#) [Windows Media Player](#) [Real Player](#)